

Fig.1

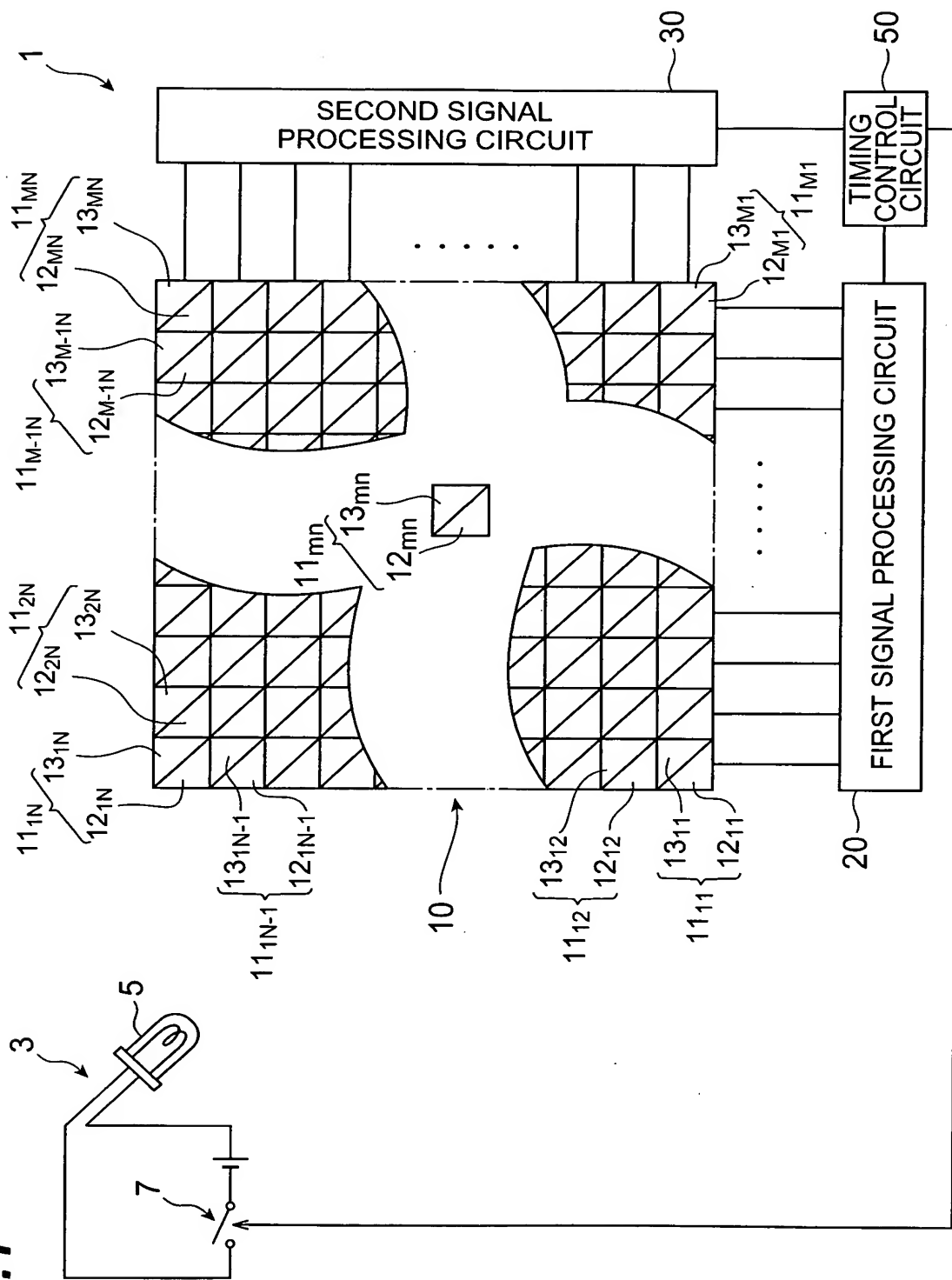


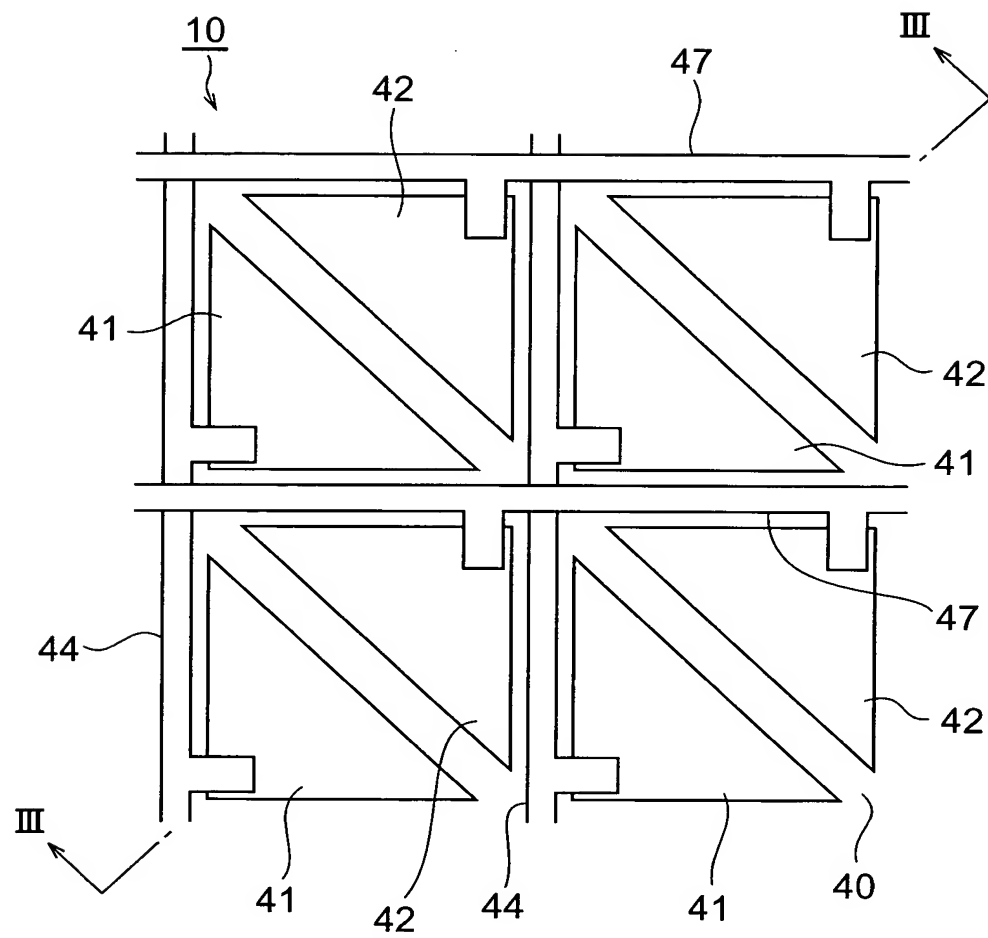
Fig.2

Fig.3

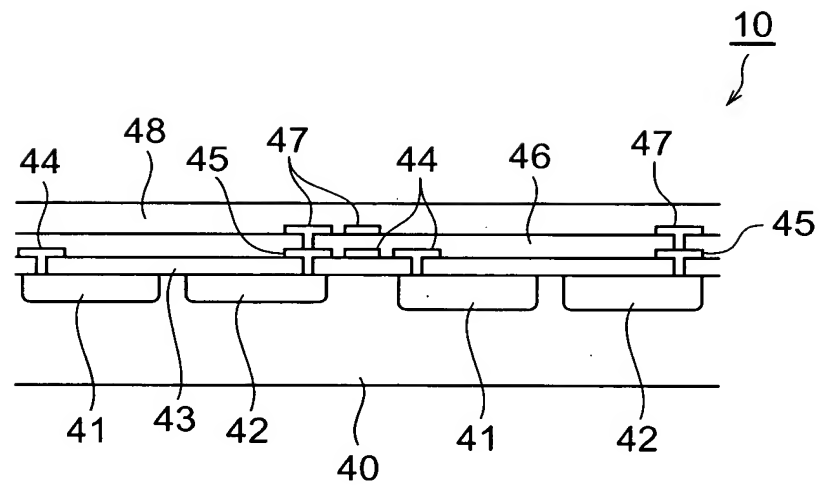


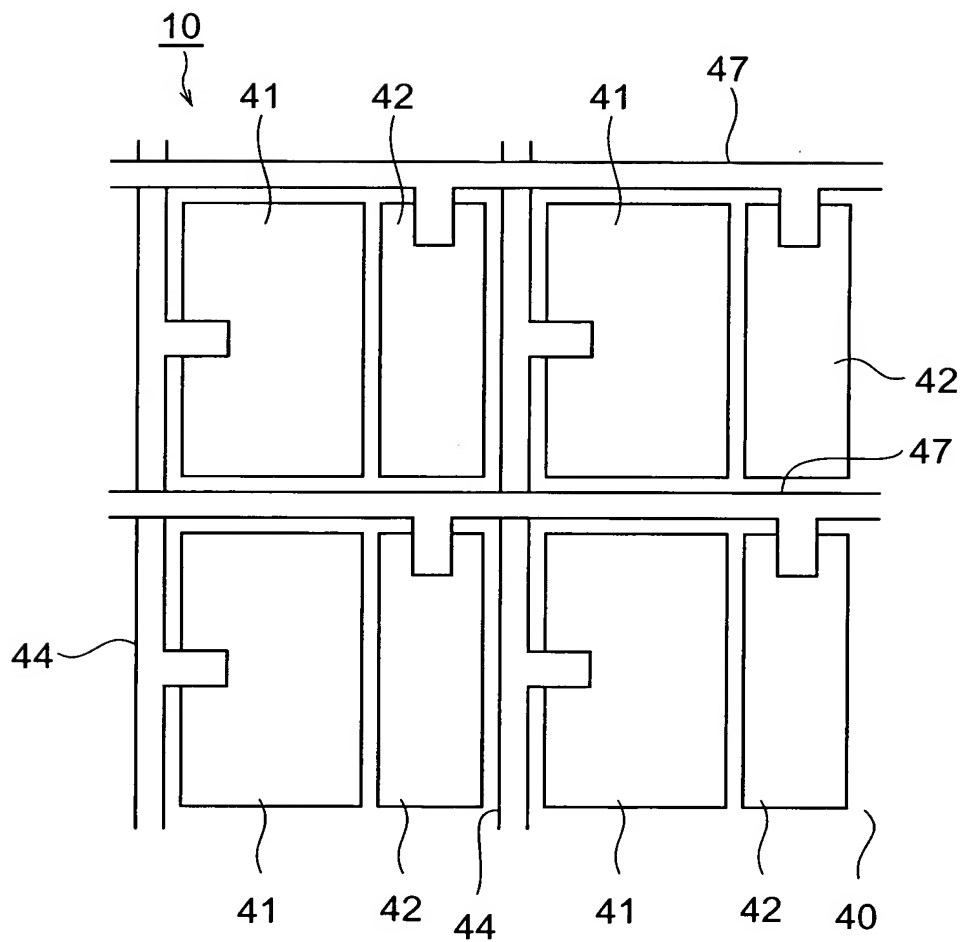
Fig.4

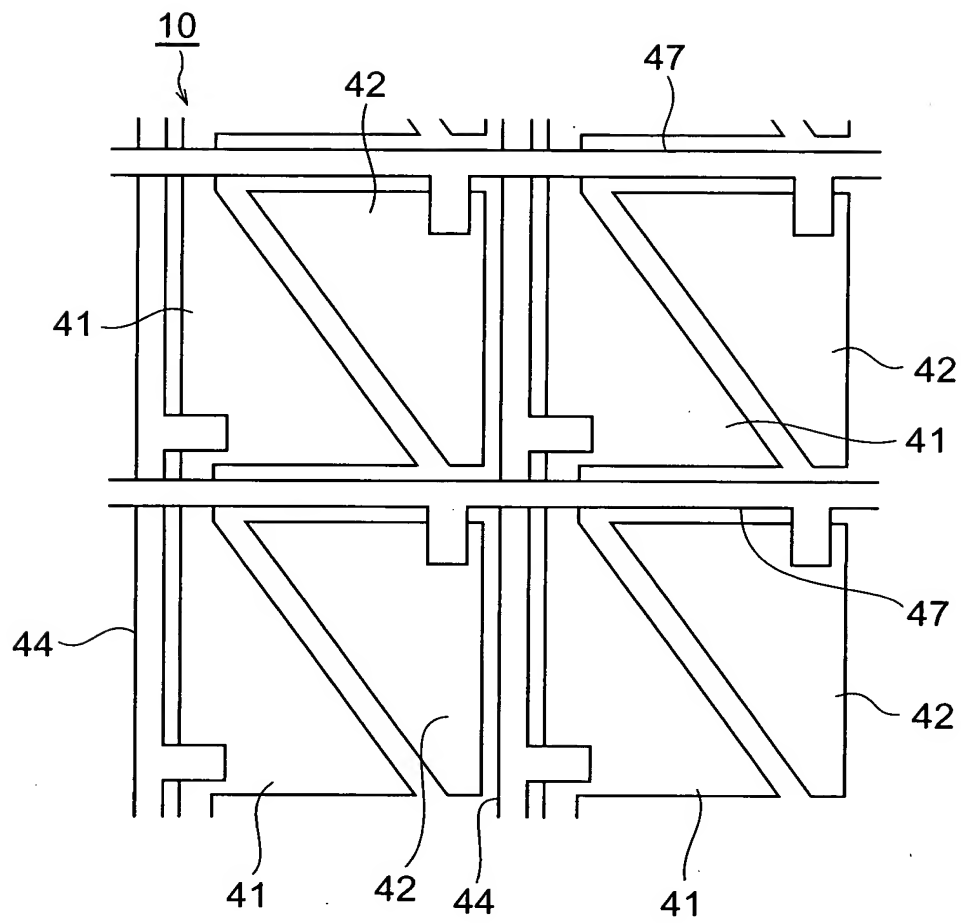
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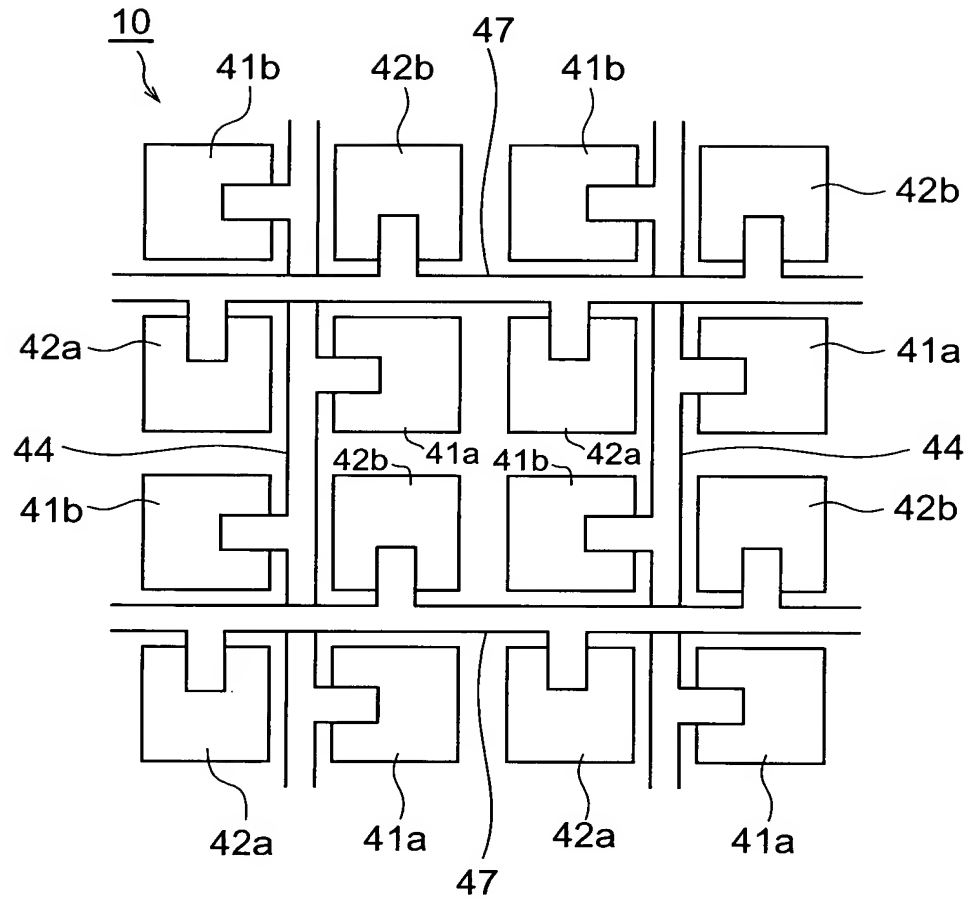
Fig.6

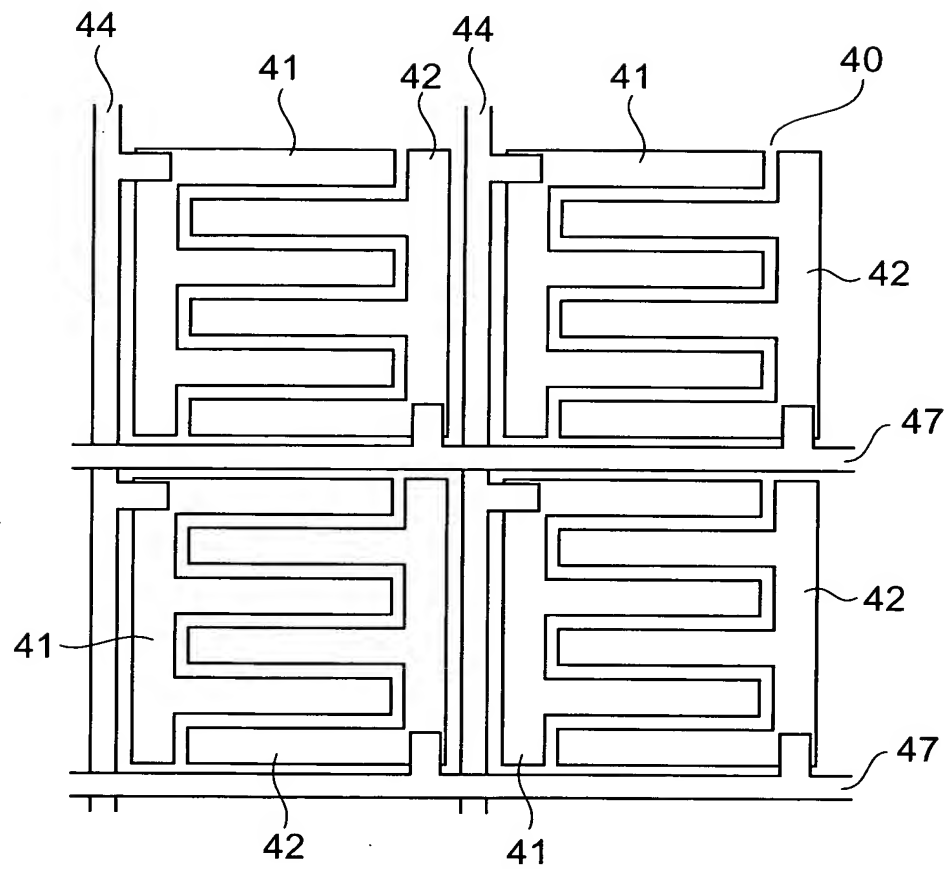
Fig.7

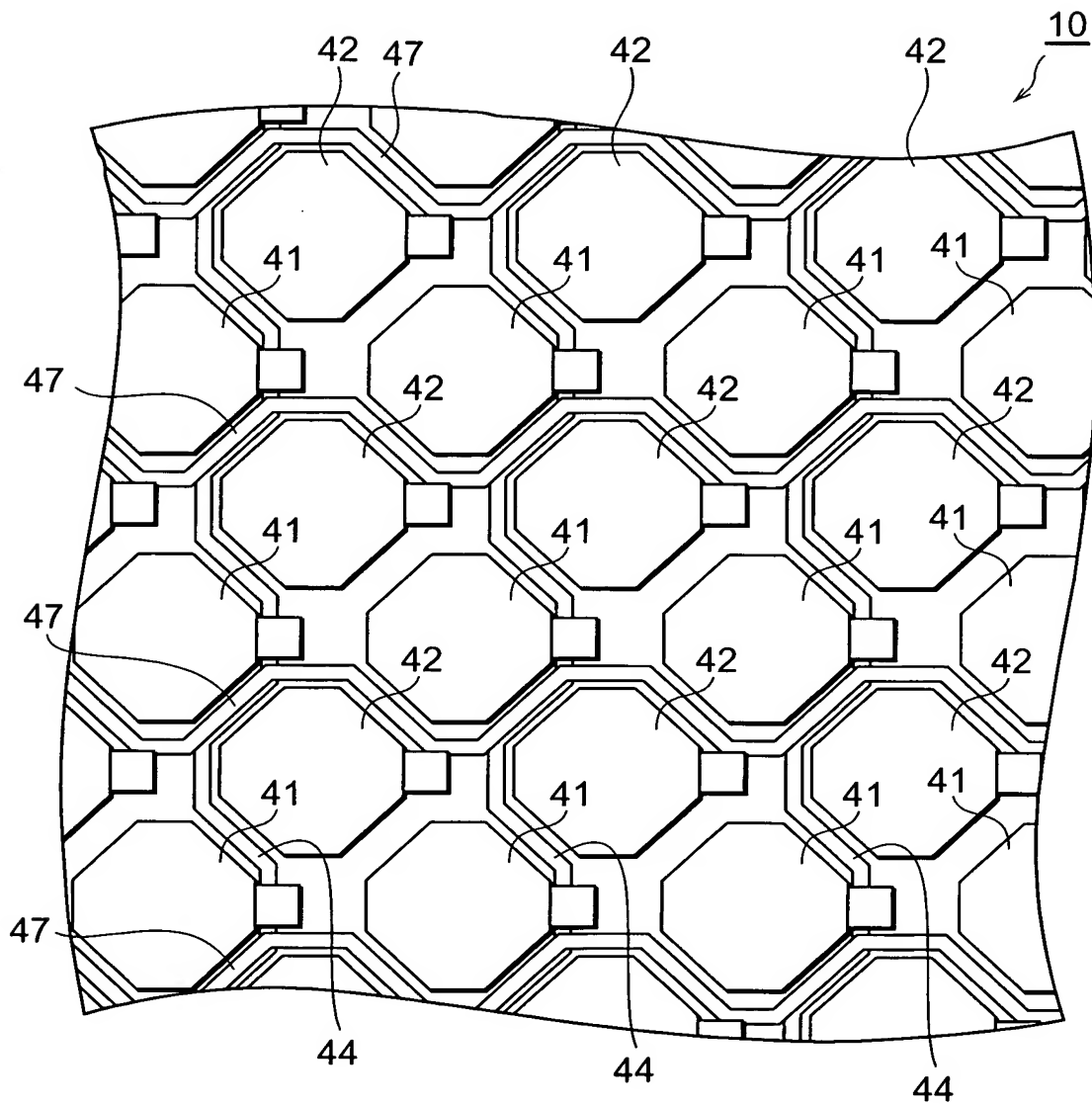
Fig. 8

Fig.9

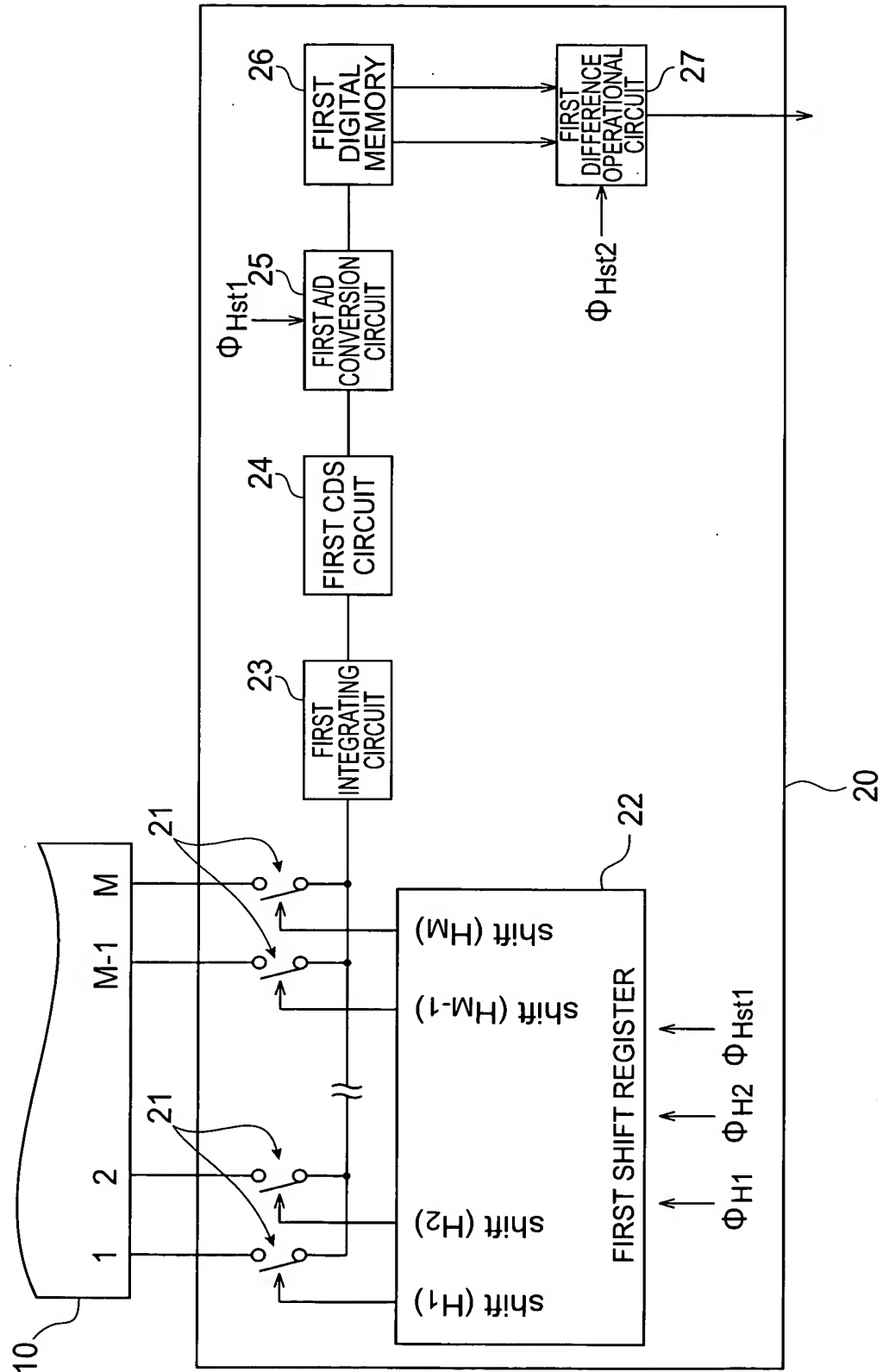
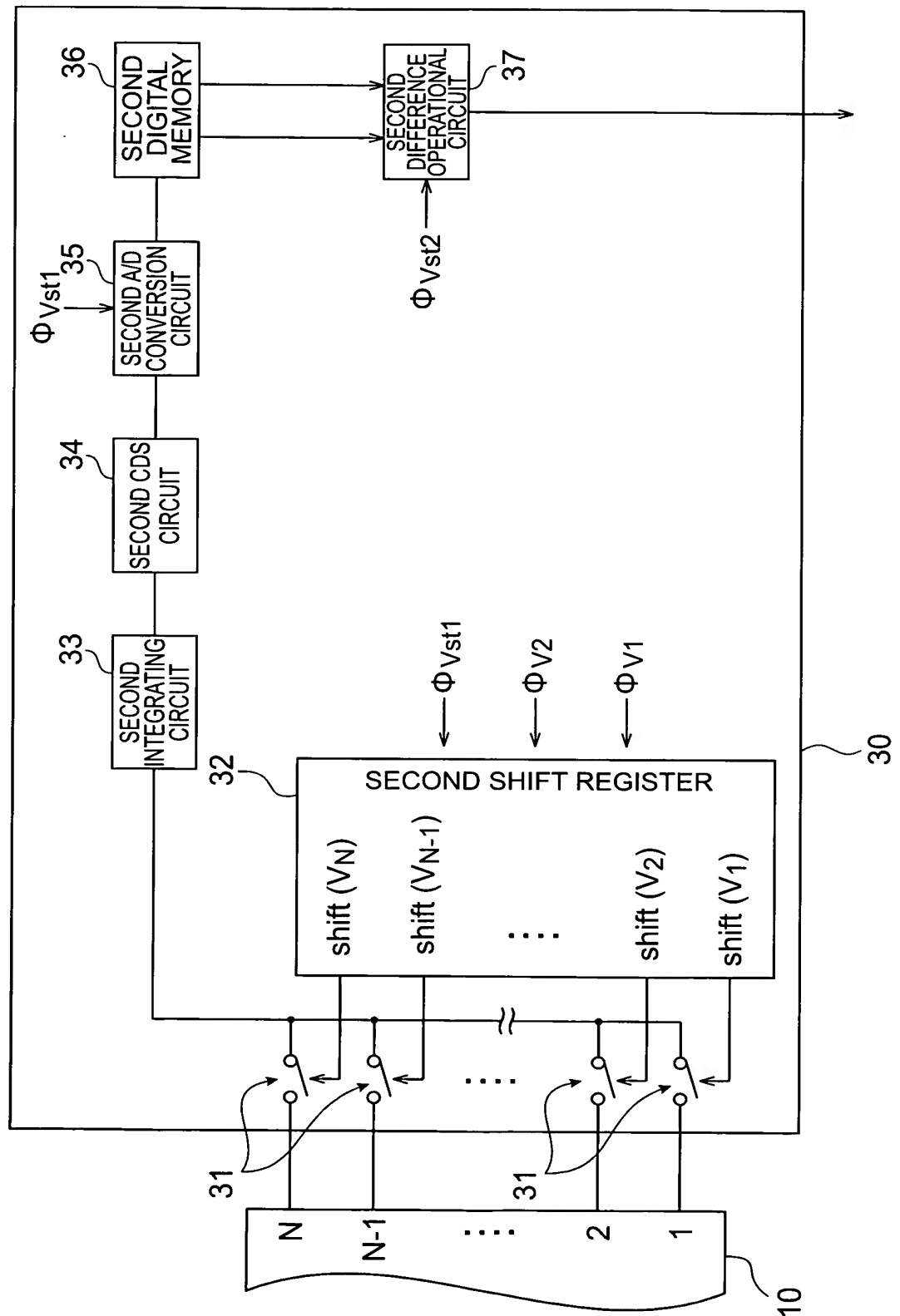


Fig. 10



FP03-0377-00

11/30

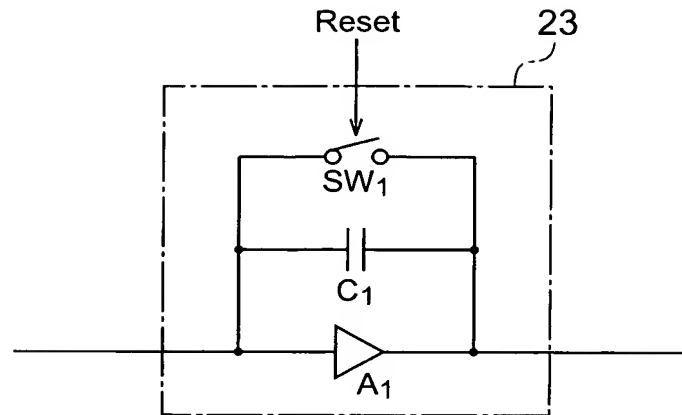
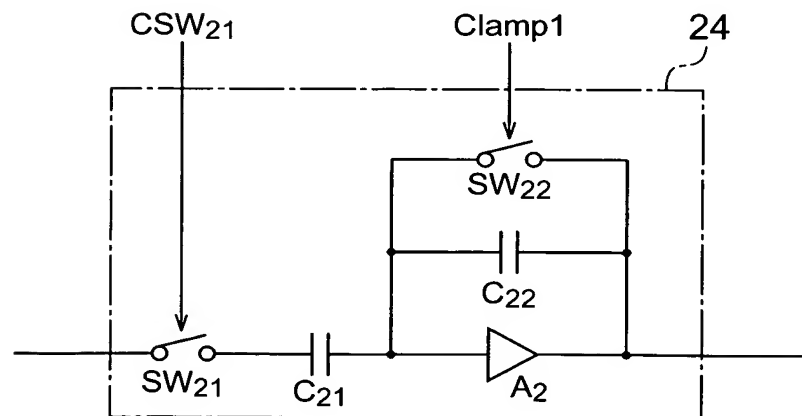
Fig.11**Fig.12**

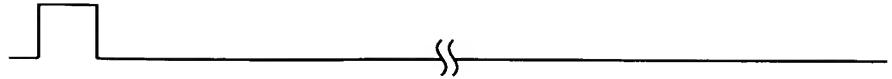
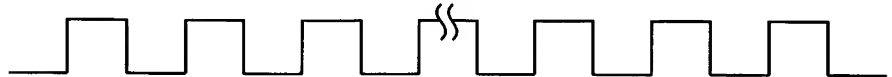
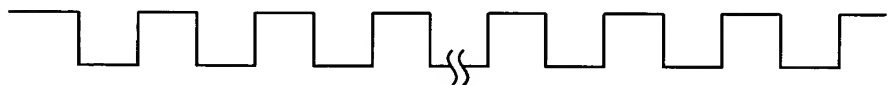
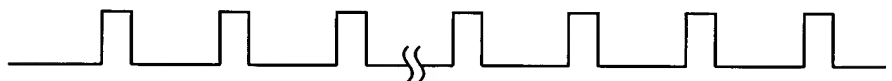
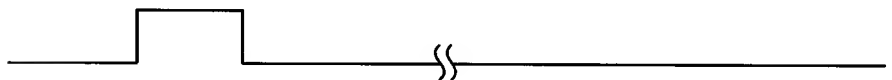
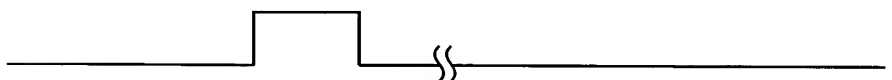
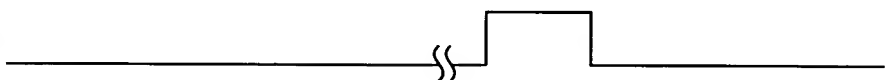
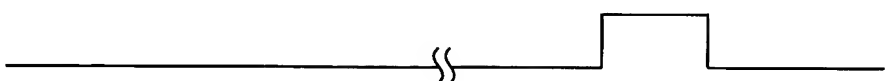
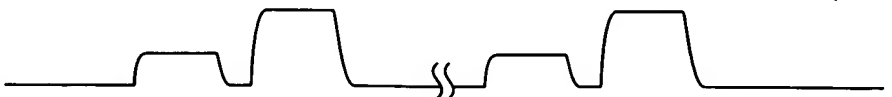
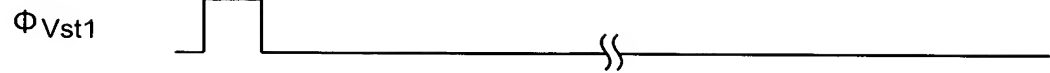
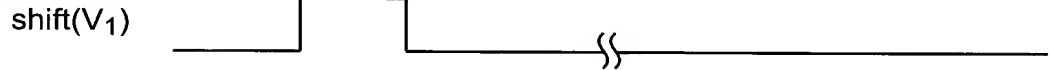
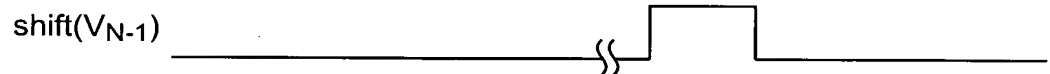
Fig.13A Φ_{Hst1} **Fig.13B** Φ_{H1} **Fig.13C** Φ_{H2} **Fig.13D** Φ_{Hreset} **Fig.13E**shift(H_1)**Fig.13F**shift(H_2)**Fig.13G**shift(H_{M-1})**Fig.13H**shift(H_M)**Fig.13I**OUTPUT OF
THE FIRST
INTEGRATING
CIRCUIT

Fig.14A**Fig.14B****Fig.14C****Fig.14D****Fig.14E****Fig.14F****Fig.14G****Fig.14H****Fig.14I**

OUTPUT OF
THE SECOND
INTEGRATING
CIRCUIT



Fig.15A Φ_{Hst1} **Fig.15B** Φ_{Hst2} **Fig.15C**

LED

Fig.15DOUTPUT OF
THE FIRST
A/D
CONVERSION
CIRCUIT **Fig.15E**OUTPUT OF
THE FIRST
DIFFERENCE
OPERATIONAL
CIRCUIT **Fig.16A** Φ_{Vst1} **Fig.16B** Φ_{Vst2} **Fig.16C**

LED

Fig.16DOUTPUT OF
THE SECOND
A/D
CONVERSION
CIRCUIT **Fig.16E**OUTPUT OF
THE SECOND
DIFFERENCE
OPERATIONAL
CIRCUIT

Fig.17

15/30

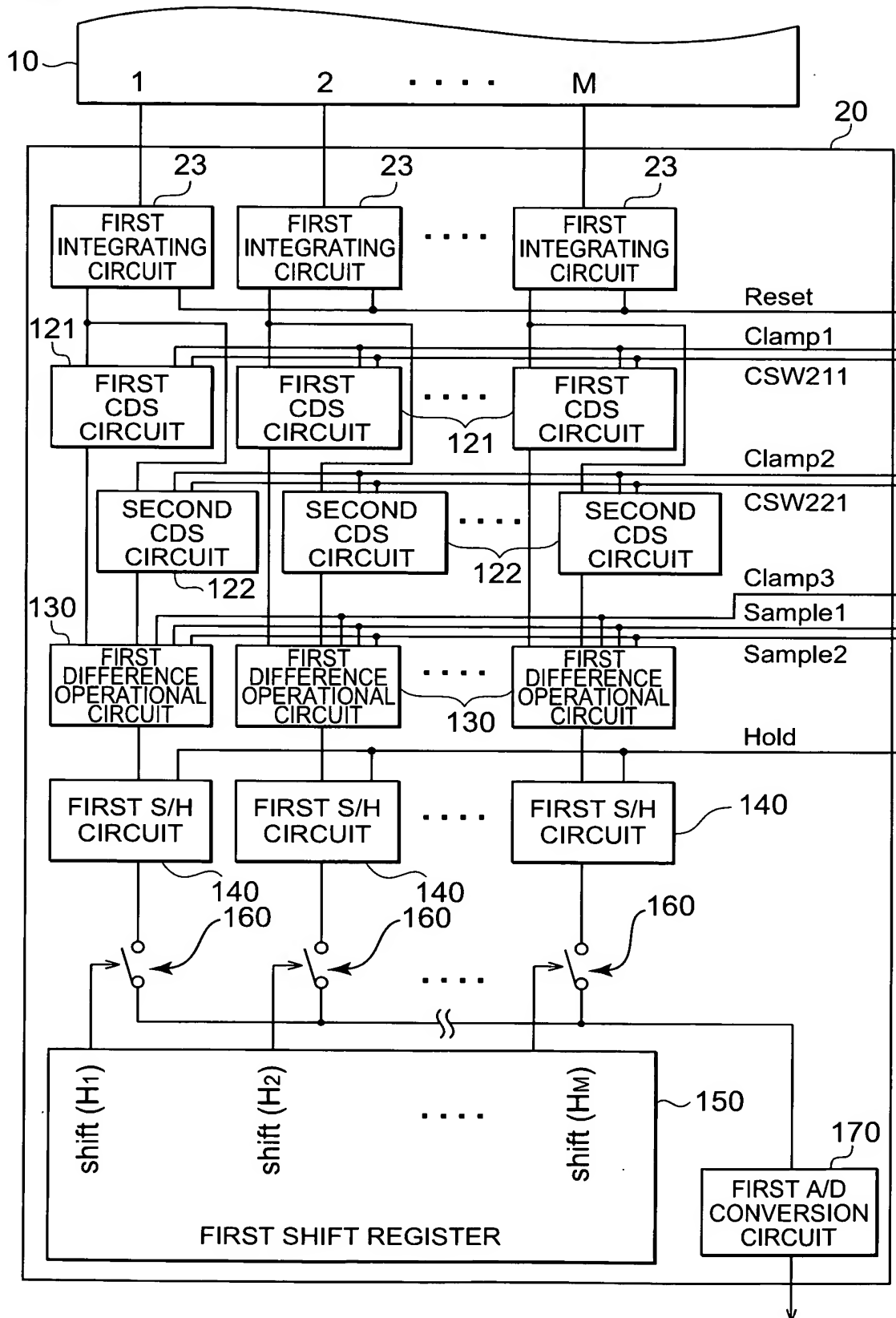


Fig.18

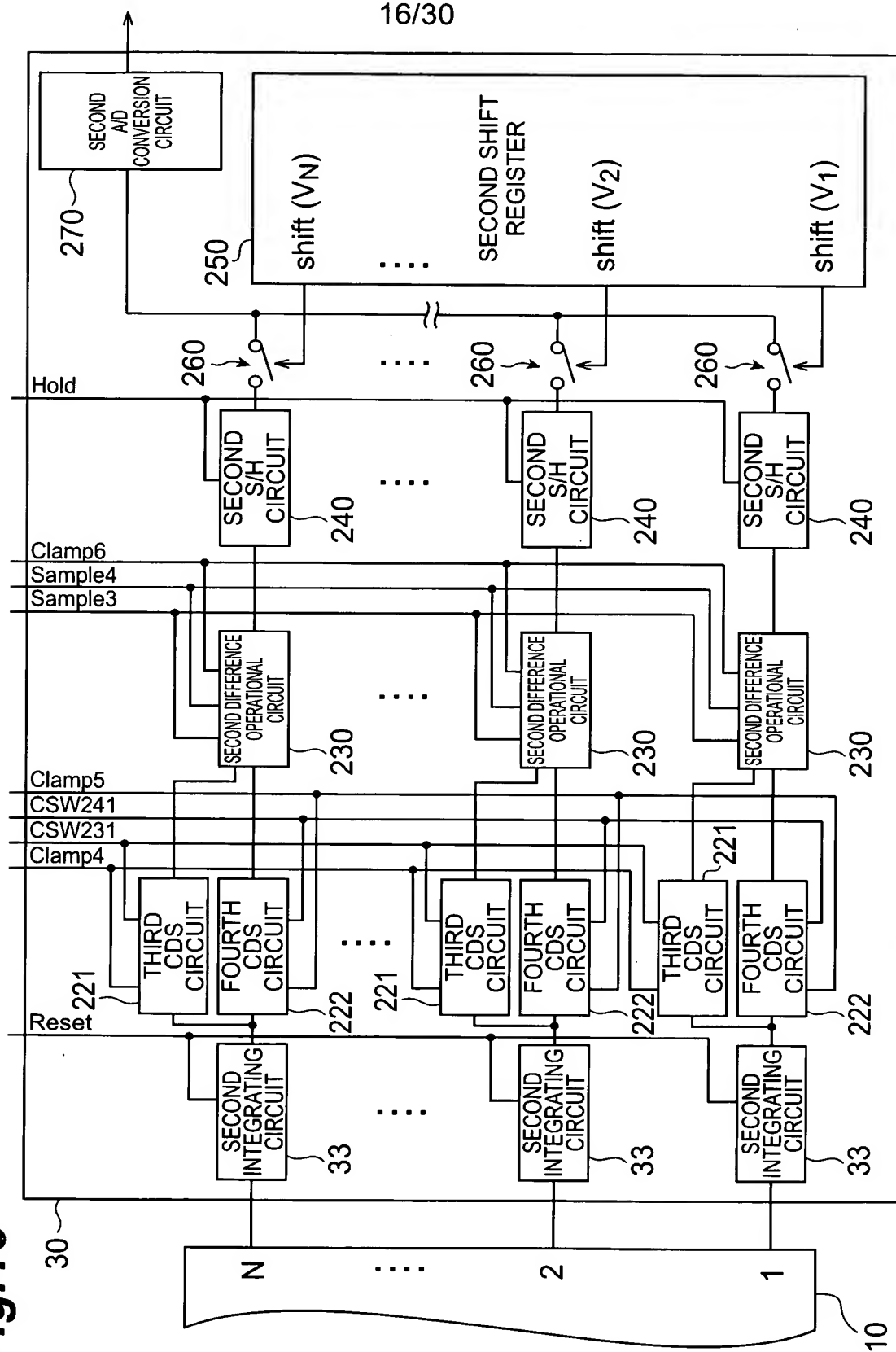
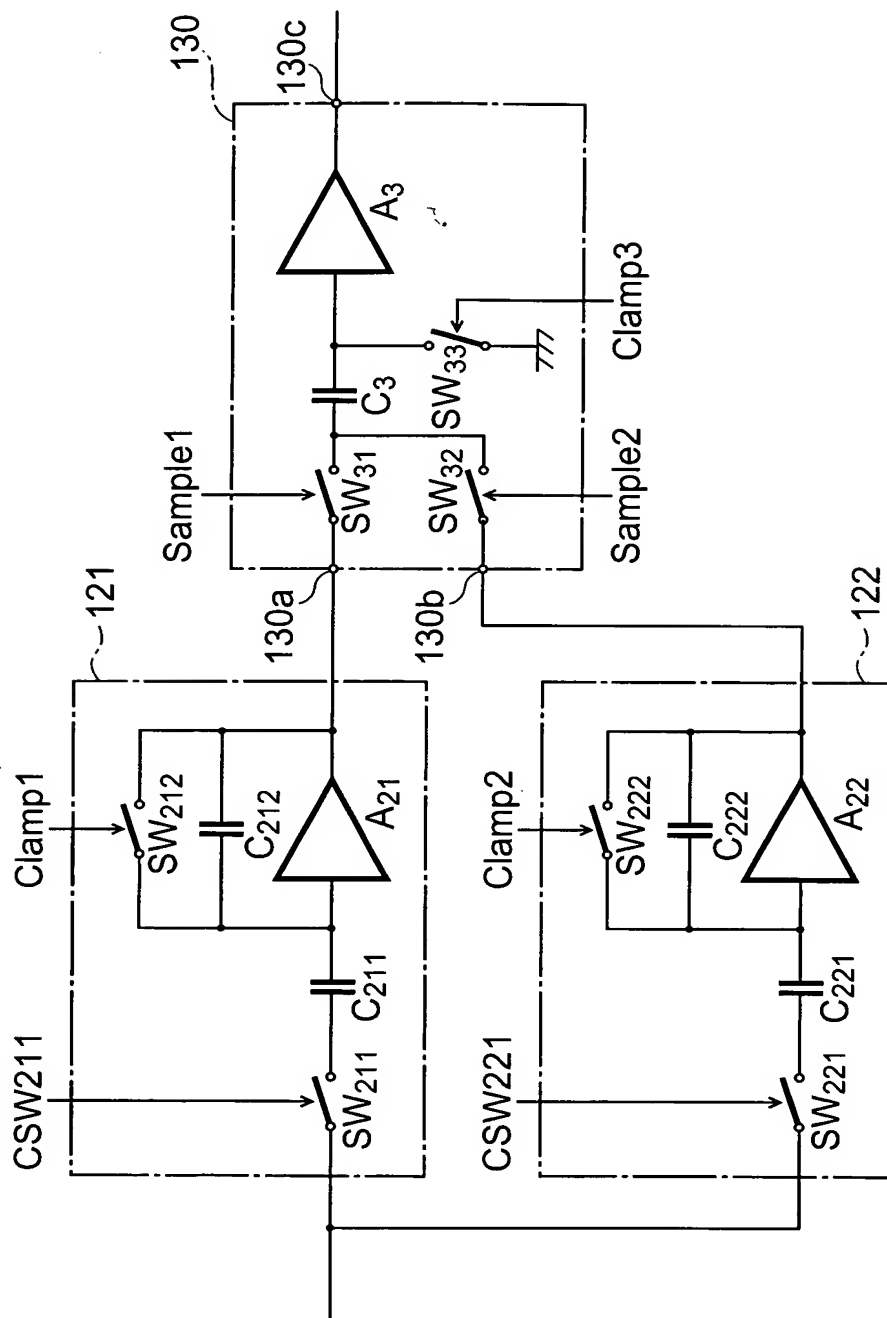


Fig.19

FP03-0377-00

18/30

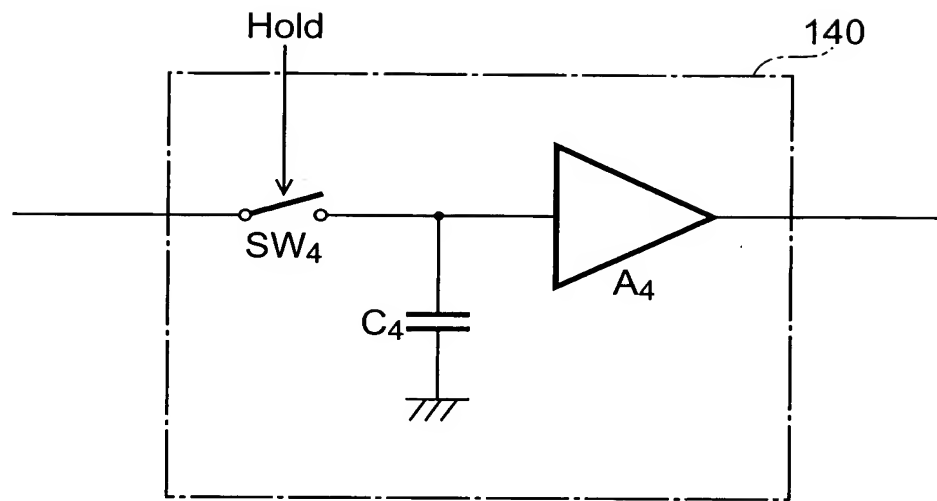
Fig.20

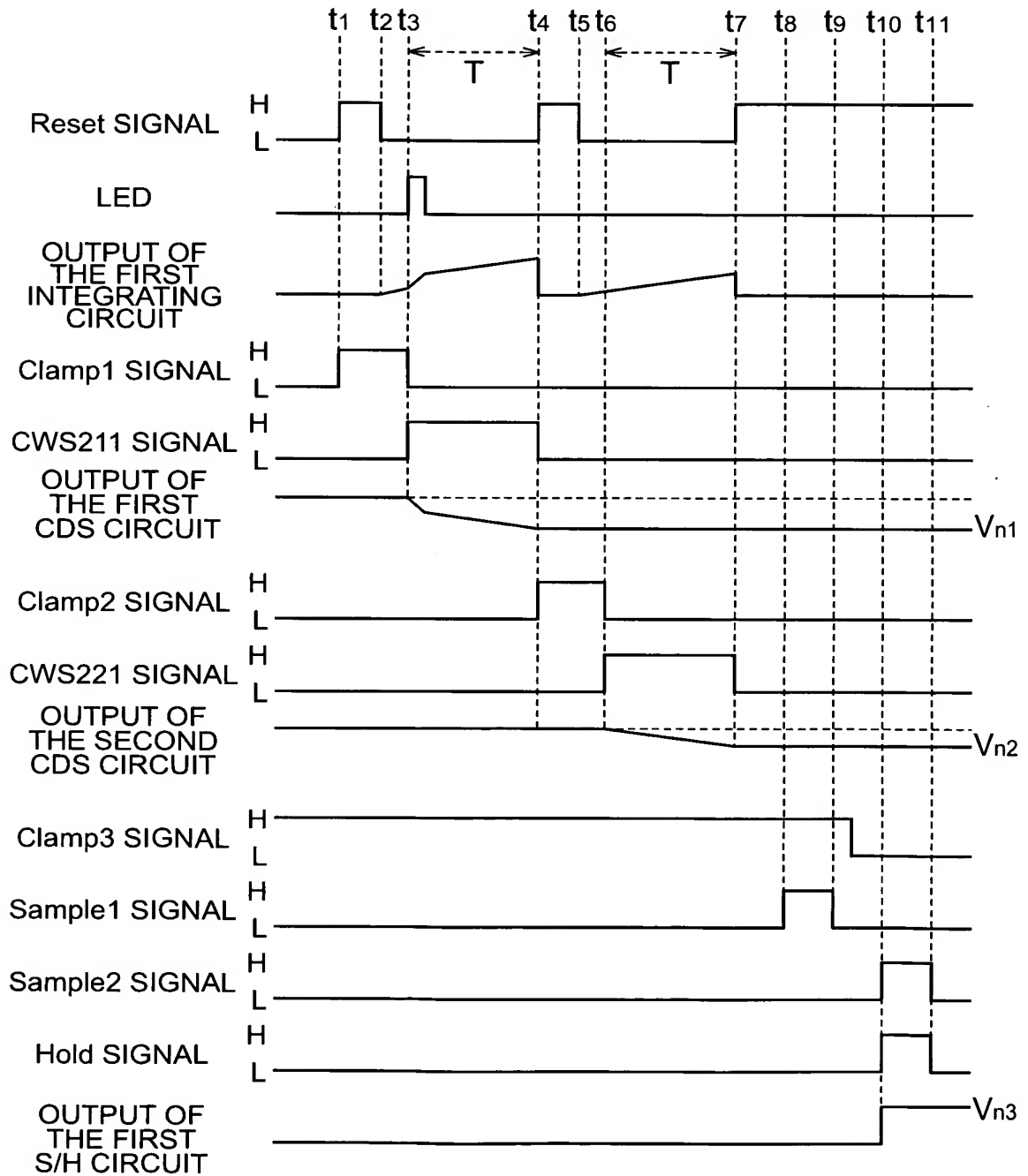
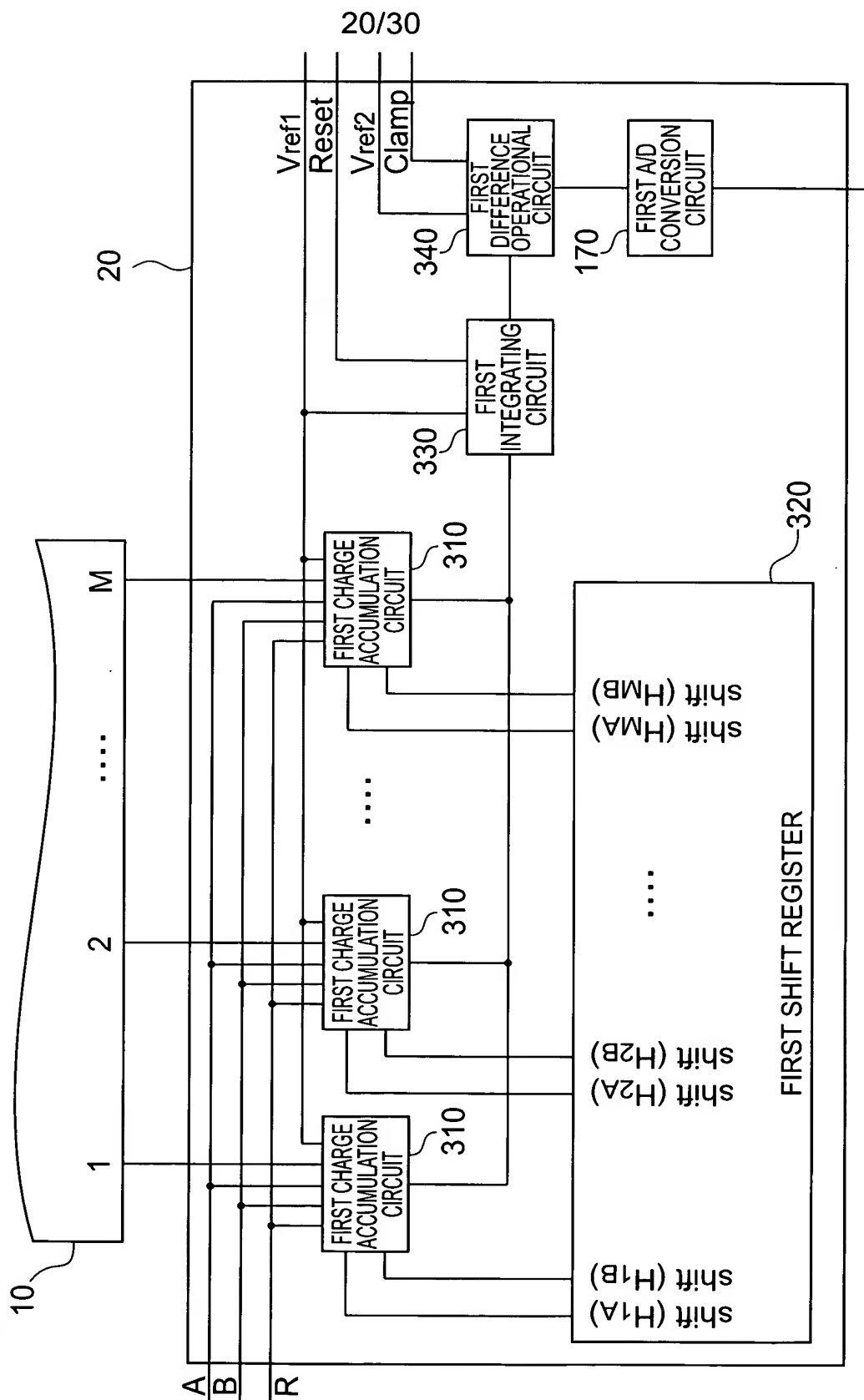
Fig.21

Fig.22



FP03-0377-00

21/30

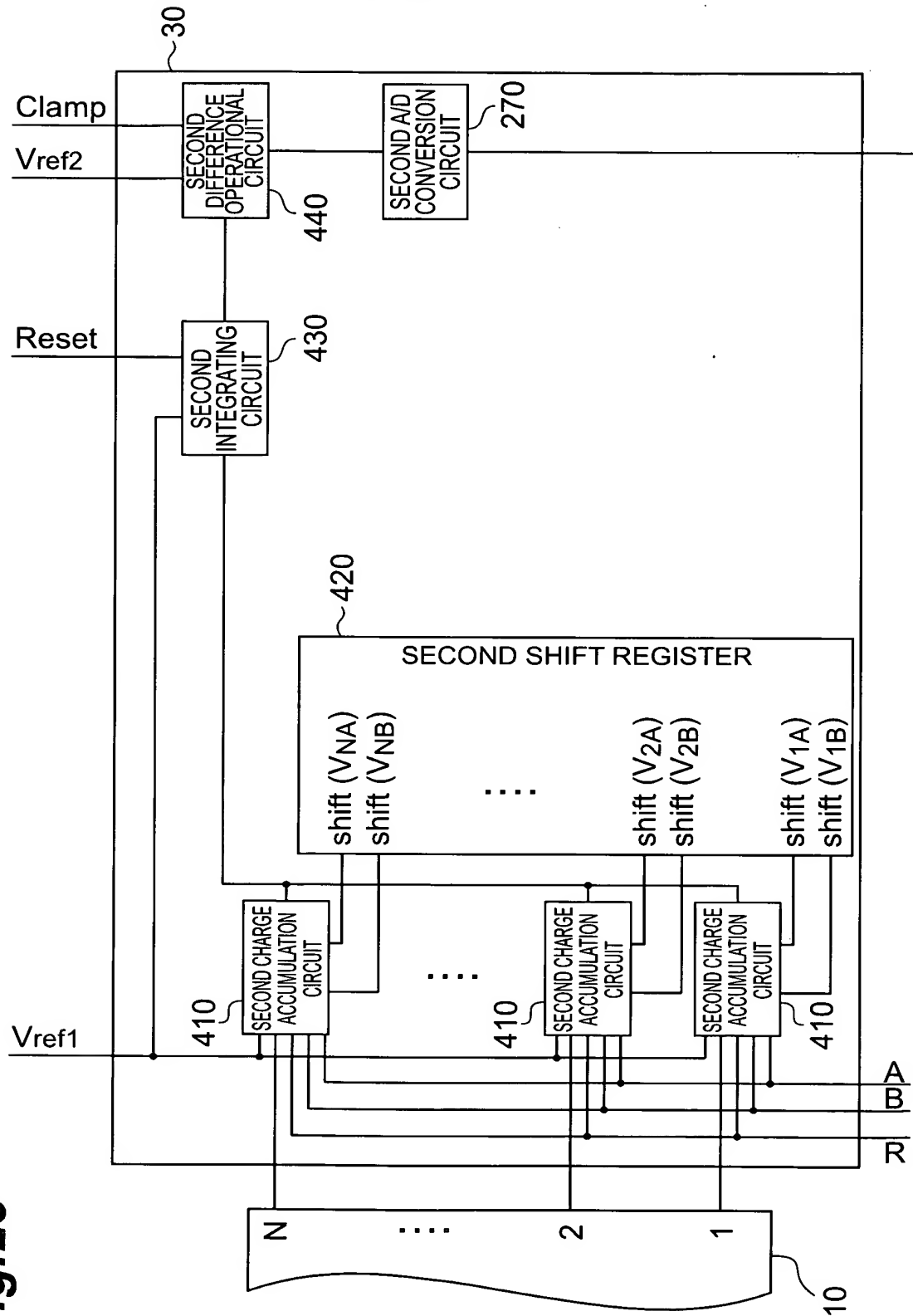


Fig. 23

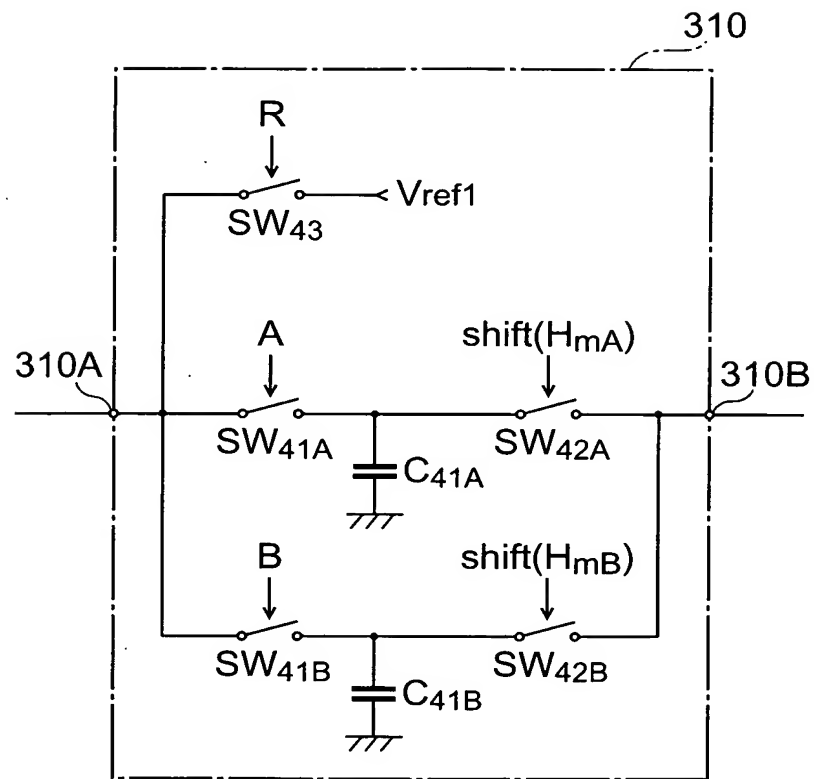
Fig.24

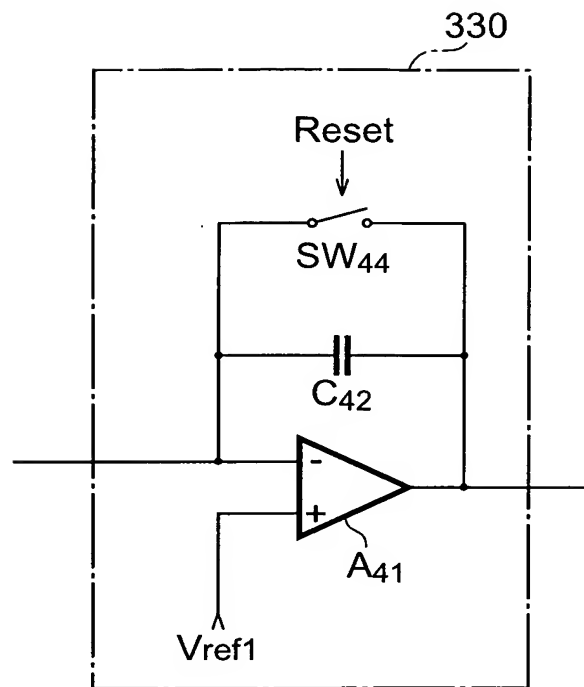
Fig.25

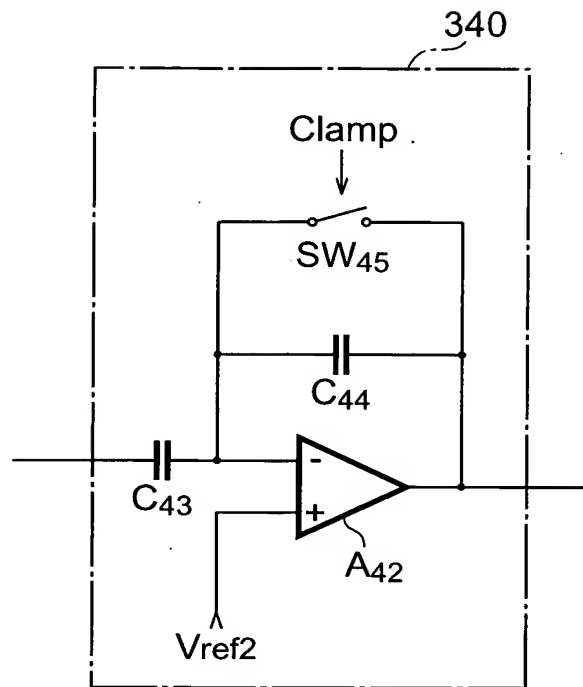
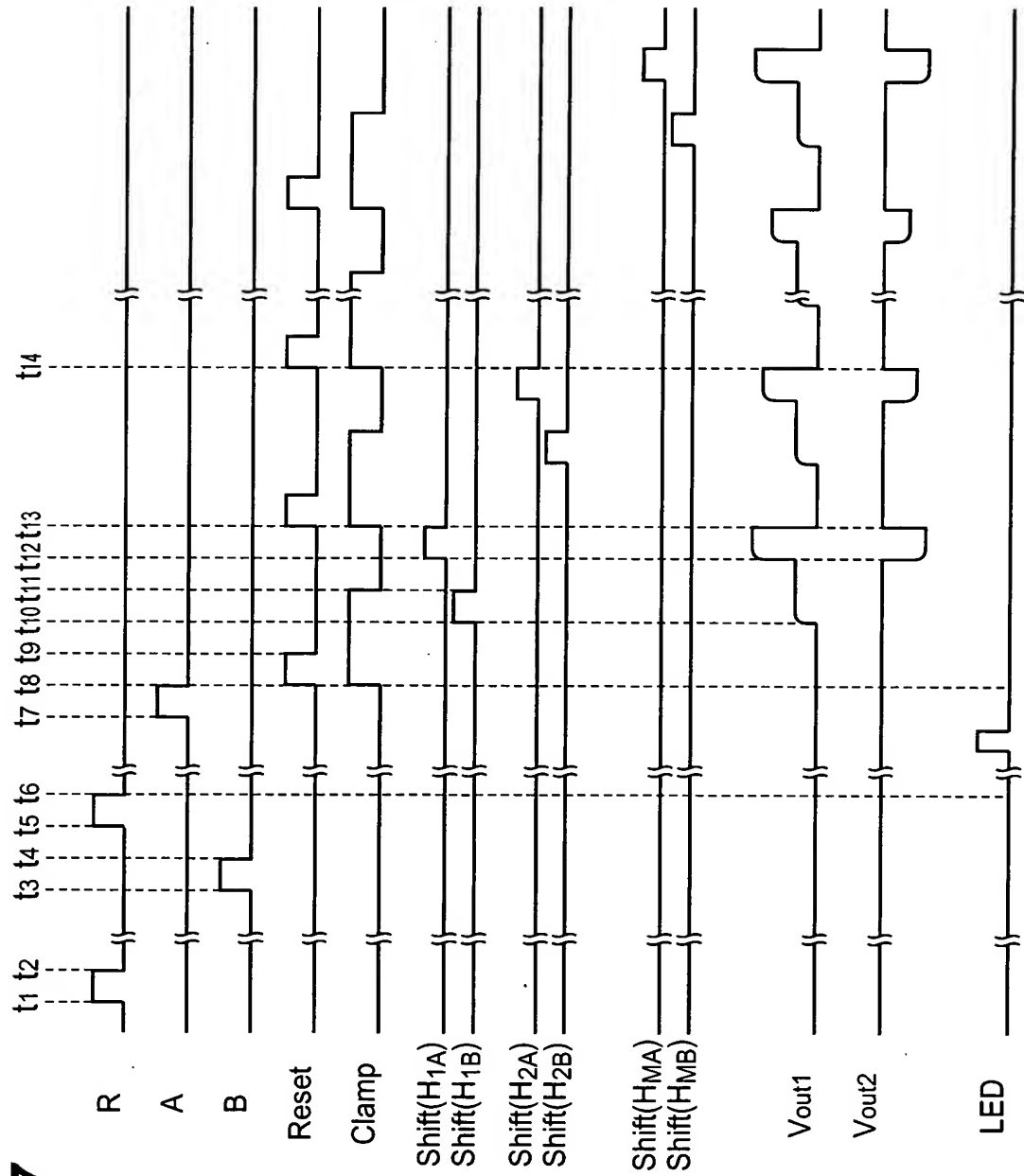
Fig.26

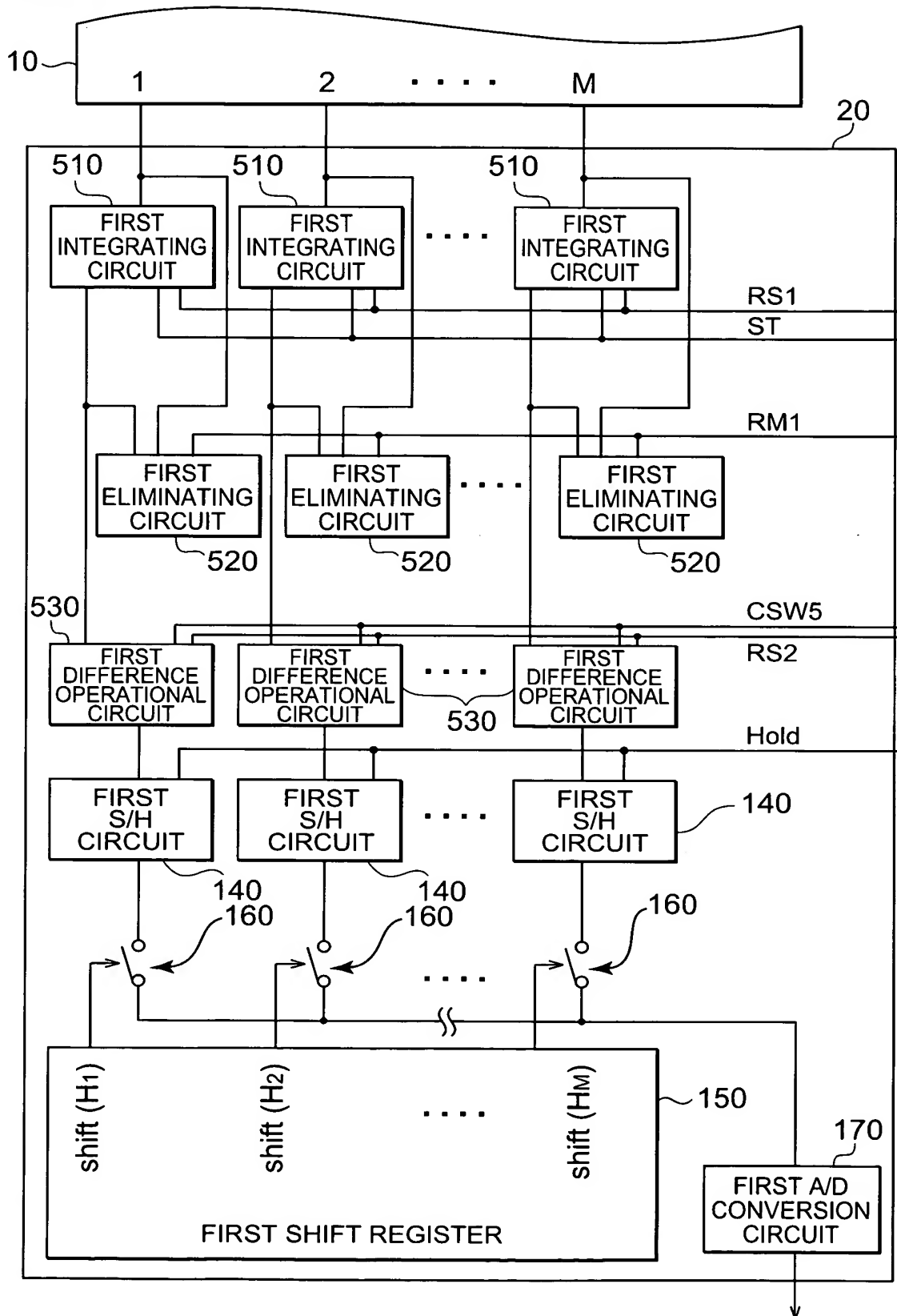
Fig.27



FP03-0377-00

Fig.28

26/30



FP03-0377-00

27/30

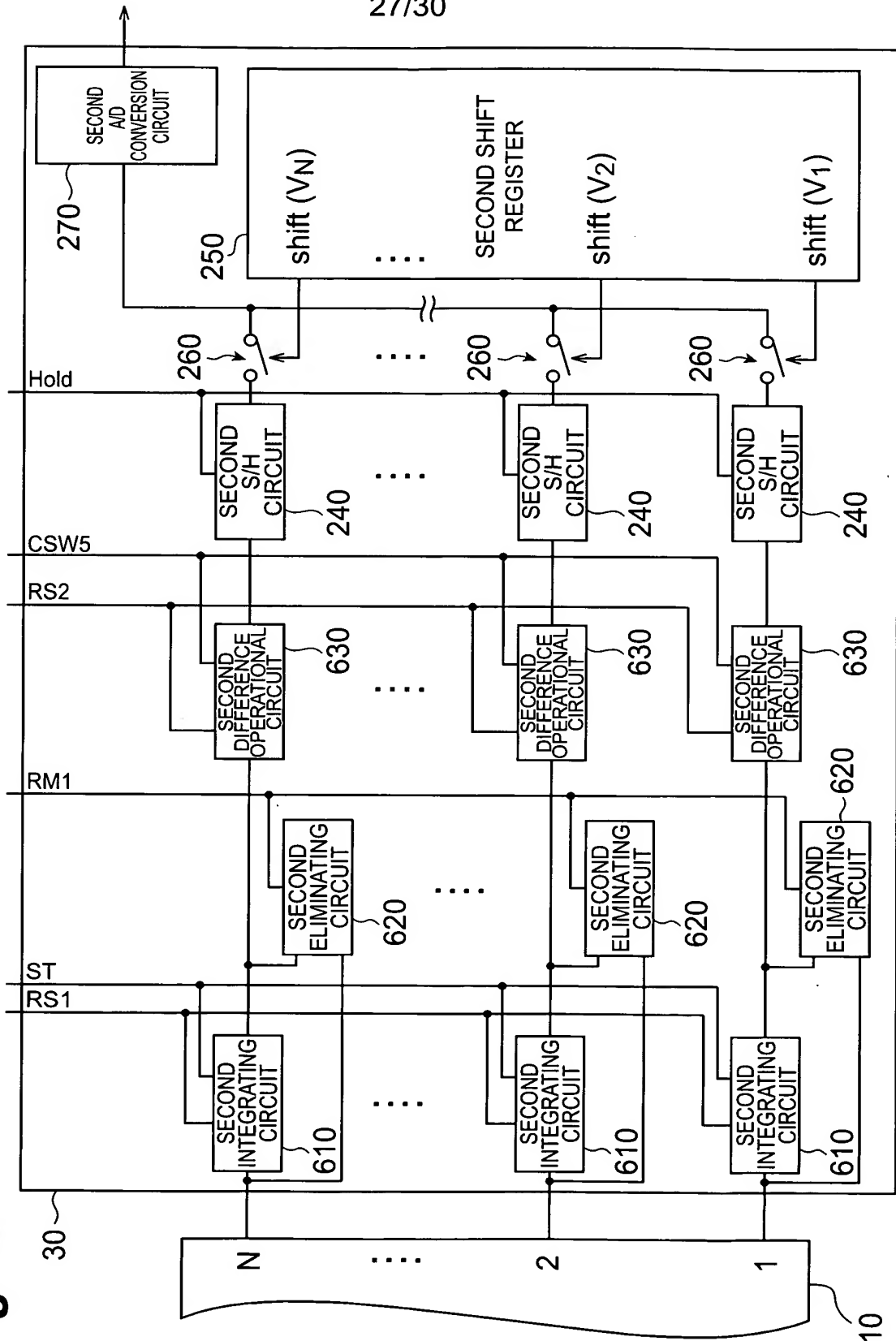


Fig. 29

Fig.30

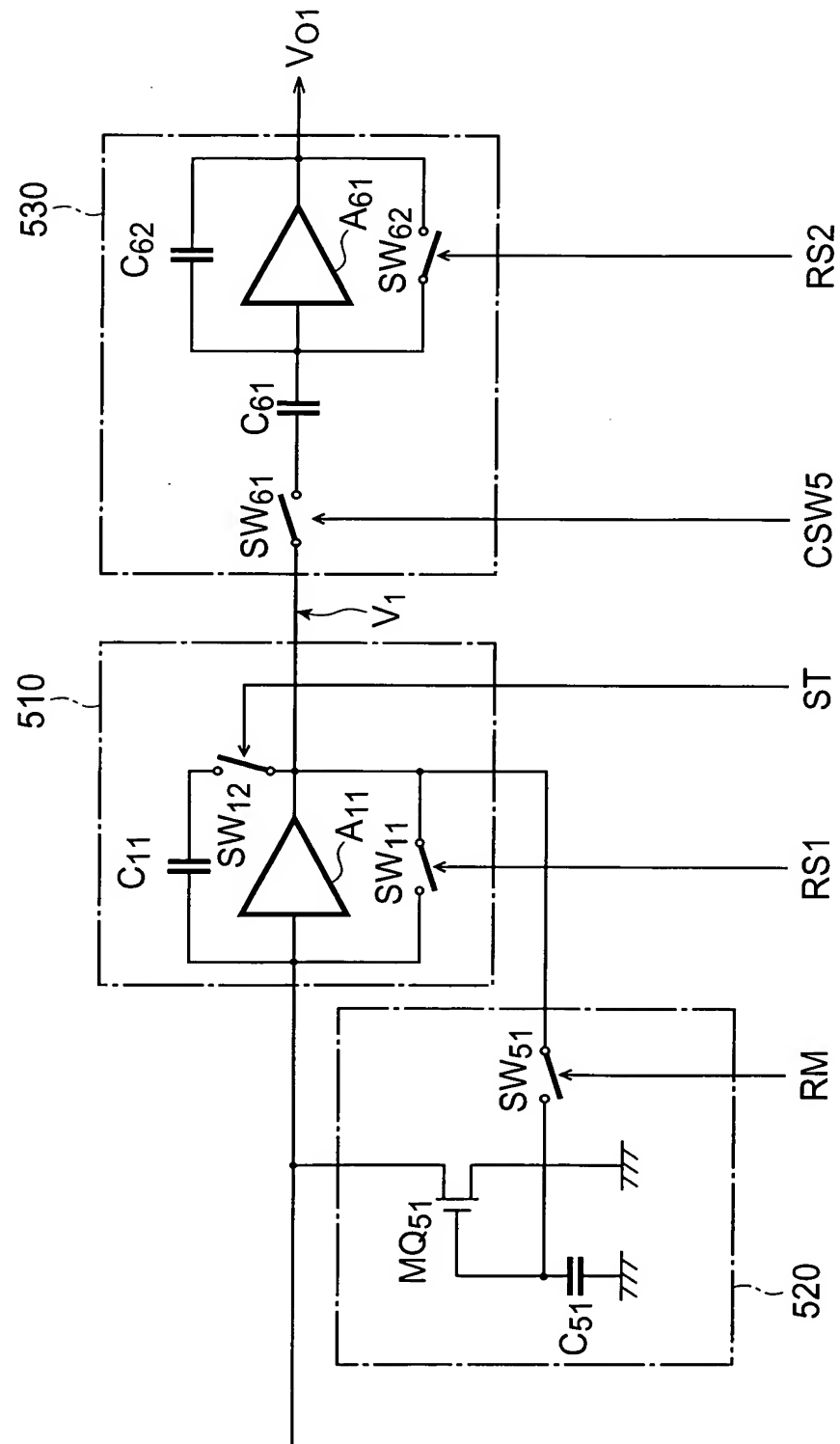


Fig.31